

# Q28QL002C00D – QSFP28 Dual Fibre

## ITU CWDM / 2km / 100G CLR4

For your product safety, please read the following information carefully before any manipulation of the transceiver:



### ESD

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all others electrical input pins, tested per MIL-STD-883G, Method 3015.4 / JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module.



### LASER SAFETY

This is a Class1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

The optical ports of the module need to be terminated with an optical connector or with a dust plug in order to avoid contamination.

## 1. Overview

Q28QL002C00D is a high performance QSFP28 transceiver module for 100 Gigabit Ethernet data links over a single mode fibre pair. The maximum reach is 2km. The four transmitters are uncooled CWDM Distributed Feedback (DFB) lasers generating four optical 25Gbps output signals, which are multiplexed together at the optical output port. The four receivers are PIN photodiodes which detect (after optical de-multiplexing) 4x 25Gbps optical input signals.

This transceiver module is compliant with the QSFP28 Multisource Agreement (MSA) and hot pluggable. Always contact Skylane Optics commercial agents for compatibility with different equipment platforms.

## 2. Features

- QSFP28 Multi-Source Agreement compliant (SFF-8636, SFF-8679)
- 100G CLR4 Alliance compliant
- Hot pluggable QSFP28 footprint (SFF-8661)
- Supports 103.125 Gbps Data Rate
- 4x 25.781Gbps Serial Electrical Interface (CAUI-4)
- Dual LC Optical Connector
- 4x uncooled CWDM DFB Transmitters
- 4x PIN Receivers
- Up to 2km Point-to-Point Transmission on Single Mode Fibre
- Operating temperature range 0°C to 70°C
- Power Dissipation <3.5W
- Single +3.3V Power Supply

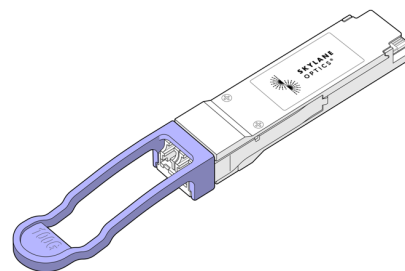


Figure 1. QSFP28 Dual Fibre (non-binding illustration)

## 3. Applications

- 100 Gigabit Ethernet

## 4. Optical Interface

P/N	Nominal Wavelength [nm]	Protocol	Optical Output Power <sup>1</sup> [dBm]	Stressed Receiver Sensitivity <sup>2</sup> (OMA) [dBm]	Optical Receiver Overload <sup>3</sup> [dBm]	Link Length <sup>1,4</sup> [km]
Q28QL002C00D	1271/1291/1311/1331	100GBASE	-0.5 to 8.5	≤ -6.8	4.5	≤ 2

1. EOL over operating temperature range
2. 25.78Gbps, BER≤10<sup>-12</sup>, PRBS 2<sup>31</sup>-1, each lane
3. The optical input to each lane of the receiver should not exceed this value. Transmitters must never be directly connected to receivers before ensuring that proper optical attenuation is used
4. Cabled optical fibre as per 100G CLR4 Alliance Specification

## 5. Technical Parameters

### 5.1. Recommended Operating Conditions

Parameter	Min	Typ	Max	Unit	Notes
Storage temperature	-40		85	°C	
Operating Case Temperature	0		70	°C	
Relative Humidity	0		85	%	Non-Condensing
Power Supply Voltage	3.135	3.3	3.465	V	
Power Supply Current			1000	mA	
Power Dissipation			3.5	W	

### 5.2. Transmitter Optical Specifications

Parameter	Min	Typ	Max	Unit	Notes
Data Rate, each Lane		25.78125		Gbps	5
Aggregated Data Rate		103.125		Gbps	5
Total Average Output Power			8.5	dBm	6
Average Output Power, each Lane	-6.5		2.5	dBm	6
Launched OMA, each Lane	-4.0		2.5		6,7
Launched OMA minus TDP, each lane	-5.0			dBm	6
Centre Wavelength, Optical Lanes 0 to 3	1264.5	1271	1277.5	nm	
	1284.5	1291	1297.5		
	1304.5	1311	1317.5		
	1324.5	1331	1337.5		
Transmitter and Dispersion Penalty (TDP), each Lane			3.3	dB	
Extinction Ratio, each Lane	3.5			dB	

5. IEEE 802.3-2012
6. Output power coupled into a 9/125 µm single mode fibre
7. Even if the TDP is <1 dB, the minimum OMA must exceed -4.0dBm

### 5.3. Receiver Optical Specifications

Parameter	Min	Typ	Max	Unit	Notes
Operating Wavelength, Optical Lanes 0 to 3	1264.5	1271	1277.5	nm	
	1284.5	1291	1297.5		
	1304.5	1311	1317.5		
	1324.5	1331	1337.5		
Average Receive Power, each Lane	-10		2.5	dBm	8
Receive Power (OMA), each Lane			2.5	dBm	
Difference in receive power between any two lanes (OMA)			5.5	dB	
Receiver Sensitivity (OMA), each Lane			-8.1	dBm	9
Stressed Receiver Sensitivity (OMA), each Lane			-5.6	dBm	10

8. Average receive power, each lane (min) is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance
9. Receiver sensitivity (OMA), each lane (max) is informative
10. 25.78Gbps, BER $\leq 10^{-12}$ , PRBS 2<sup>31</sup>-1

6. Electrical Connector

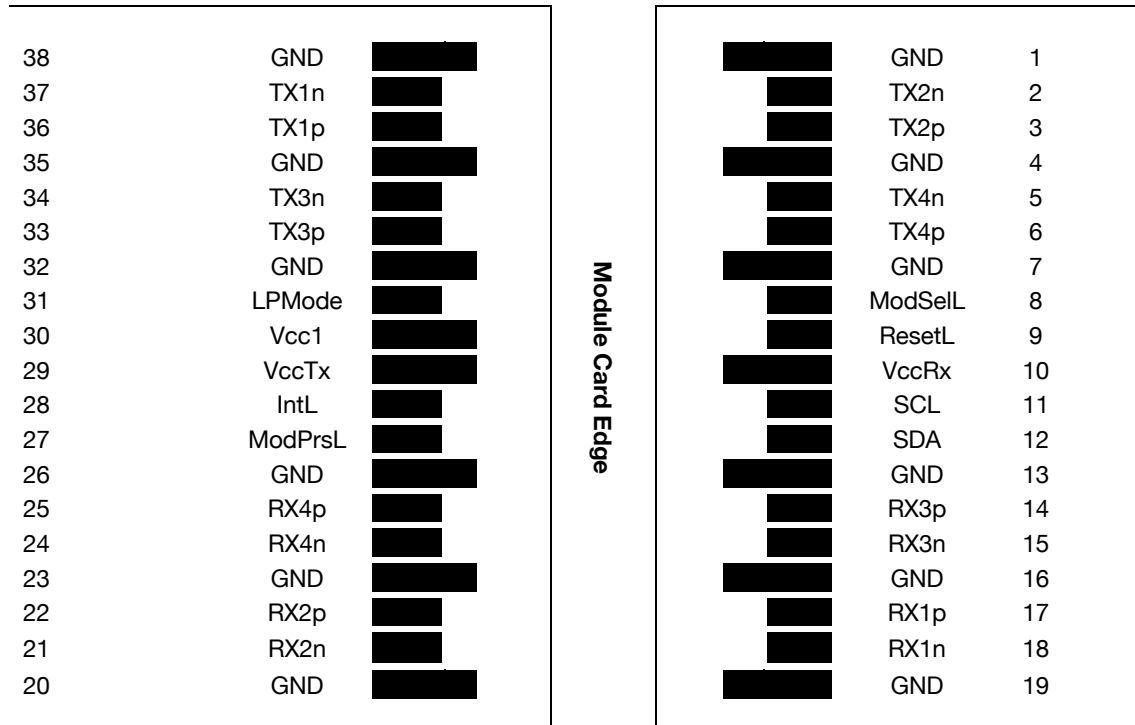


Figure 2. QSFP28 Module Pad Layout

7. Pin Function Definition

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	20	GND	Ground
2	TX2n	Transmitter Inverted Data Input	21	RX2n	Receiver Inverted Data Output
3	TX2p	Transmitter Non-Inverted Data Input	22	RX2p	Receiver Non-Inverted Data Output
4	GND	Ground	23	GND	Ground
5	TX4n	Transmitter Inverted Data Input	24	RX4n	Receiver Inverted Data Output
6	TX4p	Transmitter Non-Inverted Data Input	25	RX4p	Receiver Non-Inverted Data Output
7	GND	Ground	26	GND	Ground
8	ModSelL	Module Select	27	ModPrsL	Module Present
9	ResetL	Module Reset	28	IntL	Interrupt
10	VccRx	+3.3V Power Supply Receiver	29	VccTx	+3.3V Power supply transmitter
11	SCL	2-wire serial interface clock	30	Vcc1	+3.3V Power supply
12	SDA	2-wire serial interface data	31	LPMODE	Low Power Mode
13	GND	Ground	32	GND	Ground
14	RX3p	Receiver Non-Inverted Data Output	33	TX3p	Transmitter Non-Inverted Data Input
15	RX3n	Receiver Inverted Data Output	34	TX3n	Transmitter Inverted Data Input
16	GND	Ground	35	GND	Ground
17	RX1p	Receiver Non-Inverted Data Output	36	TX1p	Transmitter Non-Inverted Data Input
18	RX1n	Receiver Inverted Data Output	37	TX1n	Transmitter Inverted Data Input
19	GND	Ground	38	GND	Ground

## 8. EEPROM

Memory map as per SFF-8436

2-Wire Serial  
Address :  
1010000x

0	ID and status	(3 Bytes)
2		
21	Interrupt Flags	(19 Bytes)
33	Module Monitors	(12 Bytes)
81	Channel Monitors	(48 Bytes)
85	Reserved	(4 Bytes)
97	Control	(12 Bytes)
99	Reserved	(2 Bytes)
106	Free Side Device and Channel Mask	(7 Bytes)
107	Reserved	(1 Byte)
111	Free Side Device and Channel Mask	(4 Bytes)
118	Reserved	(7 Bytes)
122	Password Change Entry Area (Optional)	(4 Bytes)
126	Password Entry Area (Optional)	(4 Bytes)
127	Page Select Byte	(1 Byte)

### Page 00

### Page 01 (Optional)

### Page 02 (Optional)

### Page 03

128	Base ID Fields	(64 Bytes)
191		
223	Extended ID	(32 Bytes)
255	Vendor Specific ID	(32 Bytes)

128	CC_APPS	(1 Byte)
128	AST Table Length (TL)	(1 Byte)
129	Application Code Entry 0	(2 Bytes)
131	Application Code Entry 1	(2 Bytes)
133	Other Entries	
255	Application Code Entry TL	(2 Bytes)

128	User EEPROM Data	(128 Bytes)
255		

128	Module Threshold	(48 Bytes)
175		
223	Channel Threshold	(48 Bytes)
225	Reserved	(2 Bytes)
241	Vendor Specific Channel Control	(16 Bytes)
253	Channel Monitor Masks	(12 Bytes)
255	Reserved	(2 Bytes)

Figure 3. QSFP28 Memory Map

# Datasheet

Q28QL002C00D.docx



## 9. Ordering Information

Part Number	Description
Q28QL002C00D	QSFP28 CLR4, CWDM, Tx (DFB), Rx (PIN), maximum distance 2km on SMF, 100 Gigabit Ethernet, dual LC connector, 0°C to 70°C, DDM

## 10. Document Revision Information

Revision	Description
A	Initial release

Skylane Optics supplies a broad range of optical transceivers. Our engineers work closely with our customers to find the best solutions for every application. We are committed to provide high quality products and services to our customers.

For questions on this product please contact:  
**support@skylaneoptics.com**

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**Reliable Alliance**

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