

# QFP85P3040PD – QSFP+ Parallel Fibre

## 850nm / 300m / 40 Gigabit Ethernet / SR-4

For your product safety, please read the following information carefully before any manipulation of the transceiver:



**ESD**

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all others electrical input pins, tested per MIL-STD-883G, Method 3015.4 /JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module.



**LASER SAFETY**

This is a Class1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

The optical ports of the module need to be terminated with an optical connector or with a dust plug in order to avoid contamination.

### 1. Overview

QFP85P3040PD is a high performance QSFP+ transceiver module for 40 Gigabit Ethernet (aggregated) data links over an OM3 multimode (50/125µm) ribbon fibre. The maximum reach<sup>1</sup> is 300m, with 8.3dB end of life (EOL) power budget. The transmitters are Vertical Cavity Surface Emitting Lasers (VCSEL) generating four independent parallel optical output signals. The receivers are PIN photodiodes.

This transceiver module is compliant with the Small Form-factor Pluggable (QSFP+) Multisource Agreement (MSA) and hot pluggable. Always contact Skylane Optics' commercial agents for compatibility with different equipment platforms.

### 2. Features

- QSFP+ Multi-Source Agreement compliant (SFF-8436)
- Hot pluggable QSFP+ footprint
- Serial ID functionality supported according to (SFF-8438)
- MTP/MPO optical connector
- 4x 850nm VCSEL transmitters
- Up to 300m point-to-point transmission on OM3 ribbon fibre
- Operating temperature range 0°C to 70°C
- Four Independent Full Duplex Channels
- Up to 11.2Gbps per Lane
- Low power dissipation (<1.5W)
- Digital Diagnostics Monitoring (DDM)

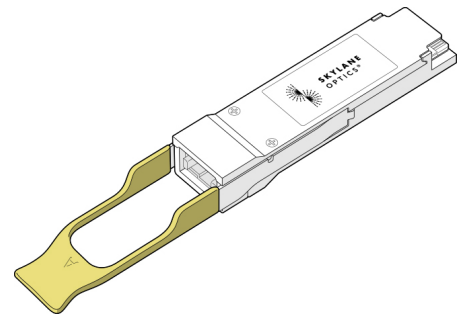


Figure 1. QSFP+ Dual Fiber 850nm (non-binding illustration)

### 3. Applications

- 40GBASE-SR4
- Infiniband QDR and DDR Interconnects
- Rack to Rack
- Data Centre

### 4. Optical Interface

P/N	Wavelength [nm]	Output Optical Power <sup>2</sup> , Each Lane [dBm]	Optical Receiver Sensitivity <sup>3</sup> , Each Lane [dBm]	Transmitter Dispersion Penalty, Each Lane [dB]	Optical Receiver Overload <sup>4</sup> , Each Lane [dBm]	Power Budget <sup>2</sup> , Each Lane [dB]
QFP85P3040PD	850nm	-2.8 to 3.0	≤ -11.1	≤ 3.5	2.4	≥ 8.3

1. Distance is estimated assuming typical optical losses after decent quality fibre deployment; Only optical budget value is guaranteed.

2. EOL, over operating temperature range. OMA

3. BER=10<sup>-12</sup>, OMA

4. The optical input to the receiver should not exceed this value. Transmitters must never be directly connected to receivers (optical loop back) before ensuring that proper optical attenuation is used.

## 5. Technical Parameters

### 5.1. Recommended Operating Conditions

Parameter	Min	Typ	Max	Unit	Notes
Storage temperature	-20		85	°C	
Operating Case Temperature	0		70	°C	
Relative Humidity	5		85	%	Non-Condensing
Power Supply Voltage	3.15	3.3	3.45	V	
Power Supply Current			455	mA	
Power Dissipation			1.5	W	

### 5.2. Transmitter Optical Specifications

Parameter	Min	Typ	Max	Unit	Notes
Average Output Power, each Lane	-7.5		1.0	dBm	5
Optical Modulation Amplitude (OMA), each Lane	-2.8		3.0		
Transmitter and Dispersion Penalty (TDP), each Lane			3.5	dB	
OMA minus TDP, each Lane	-6.5			dBm	
Centre Wavelength	840	850	860	nm	
Optical Extinction Ratio (ER)	3			dB	
Spectral Width (RMS)			0.65	nm	

5. Output power coupled into a 50/125 µm multi-mode fibre

### 5.3. Receiver Optical Specifications

Parameter	Min	Typ	Max	Unit	Notes
Operating Wavelength	830	850	860	nm	
Receiver Overload, each Lane			2.4	dBm	
Receiver Sensitivity (OMA), each Lane			-11.1	dBm	
Stressed Receiver Sensitivity OMA, each Lane			-7.5		

## 6. Electrical Connector

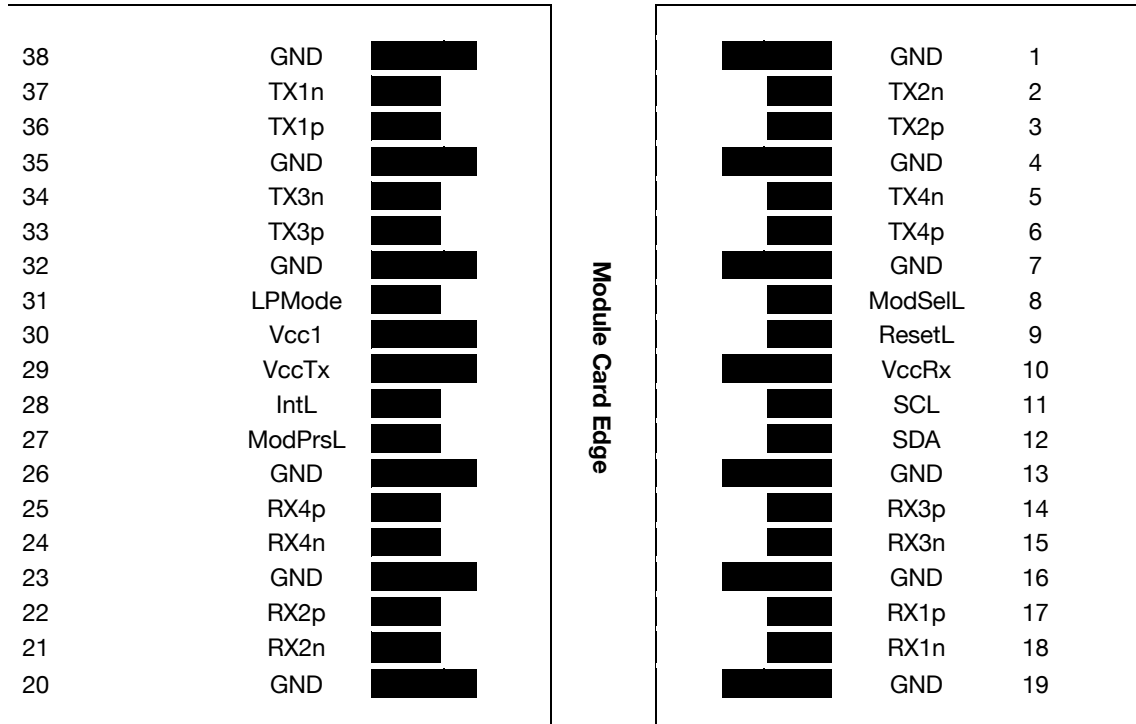


Figure 2. QSFP+ Module Pad Layout

## 7. Pin Function Definition

Pin	Symbol	Description	Pin	Symbol	Description
1	GND	Ground	20	GND	Ground
2	TX2n	Transmitter Inverted Data Input	21	RX2n	Receiver Inverted Data Output
3	TX2p	Transmitter Non-Inverted Data Input	22	RX2p	Receiver Non-Inverted Data Output
4	GND	Ground	23	GND	Ground
5	TX4n	Transmitter Inverted Data Input	24	RX4n	Receiver Inverted Data Output
6	TX4p	Transmitter Non-Inverted Data Input	25	RX4p	Receiver Non-Inverted Data Output
7	GND	Ground	26	GND	Ground
8	ModSelL	Module Select	27	ModPrsL	Module Present
9	ResetL	Module Reset	28	IntL	Interrupt
10	VccRx	+3.3V Power Supply Receiver	29	VccTx	+3.3V Power supply transmitter
11	SCL	2-wire serial interface clock	30	Vcc1	+3.3V Power supply
12	SDA	2-wire serial interface data	31	LPMODE	Low Power Mode
13	GND	Ground	32	GND	Ground
14	RX3p	Receiver Non-Inverted Data Output	33	TX3p	Transmitter Non-Inverted Data Input
15	RX3n	Receiver Inverted Data Output	34	TX3n	Transmitter Inverted Data Input
16	GND	Ground	35	GND	Ground
17	RX1p	Receiver Non-Inverted Data Output	36	TX1p	Transmitter Non-Inverted Data Input
18	RX1n	Receiver Inverted Data Output	37	TX1n	Transmitter Inverted Data Input
19	GND	Ground	38	GND	Ground

**8. EEPROM**

QSFP+ MSA (SFF-8436)

2-Wire Serial  
Address :  
1010000x

0	ID and status	(3 Bytes)
2		
21	Interrupt Flags	(19 Bytes)
33	Module Monitors	(12 Bytes)
81	Channel Monitors	(48 Bytes)
85	Reserved	(4 Bytes)
97	Control	(12 Bytes)
99	Reserved	(2 Bytes)
106	Free Side Device and Channel Mask	(7 Bytes)
107	Reserved	(1 Byte)
111	Free Side Device and Channel Mask	(4 Bytes)
118	Reserved	(7 Bytes)
122	Password Change Entry Area (Optional)	(4 Bytes)
126	Password Entry Area (Optional)	(4 Bytes)
127	Page Select Byte	(1 Byte)

**Page 00**

**Page 01 (Optional)**

**Page 02 (Optional)**

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128	Base ID Fields	(64 Bytes)
191		
223	Extended ID	(32 Bytes)
255	Vendor Specific ID	(32 Bytes)

128	CC_APPS	(1 Byte)
128	AST Table Length (TL)	(1 Byte)
129	Application Code Entry 0	(2 Bytes)
131	Application Code Entry 1	(2 Bytes)
133	Other Entries	
255	Application Code Entry TL	(2 Bytes)

128	User EEPROM Data	(128 Bytes)
255		

128	Module Threshold	(48 Bytes)
175	Channel Threshold	(48 Bytes)
223	Reserved	(2 Bytes)
225	Vendor Specific Channel Control	(16 Bytes)
241	Channel Monitor Masks	(12 Bytes)
253	Reserved	(2 Bytes)
255	Reserved	(2 Bytes)

Figure 3. EEPROM of a Compact QSFP+

**9. Ordering Information**

Part Number	Description
QFP85P3040PD	QSFP+ SR-4 40G 850nm, Tx VCSEL , Rx PIN, maximum distance 300m on OM3, power budget 8.3dB, 40 Gigabit Ethernet & Infiniband QDR, MTP/MPO connector, 0°C to 70°C, DDM

**10. Document Revision Information**

Revision	Description
RevA	Initial release

Skylane Optics supplies a broad range of optical transceivers. Our engineers work closely with our customers to find the best solutions for every application. We are committed to provide high quality products and services to our customers.

For questions on this product please contact:  
**support@skylaneoptics.com**

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